

FIG. 1

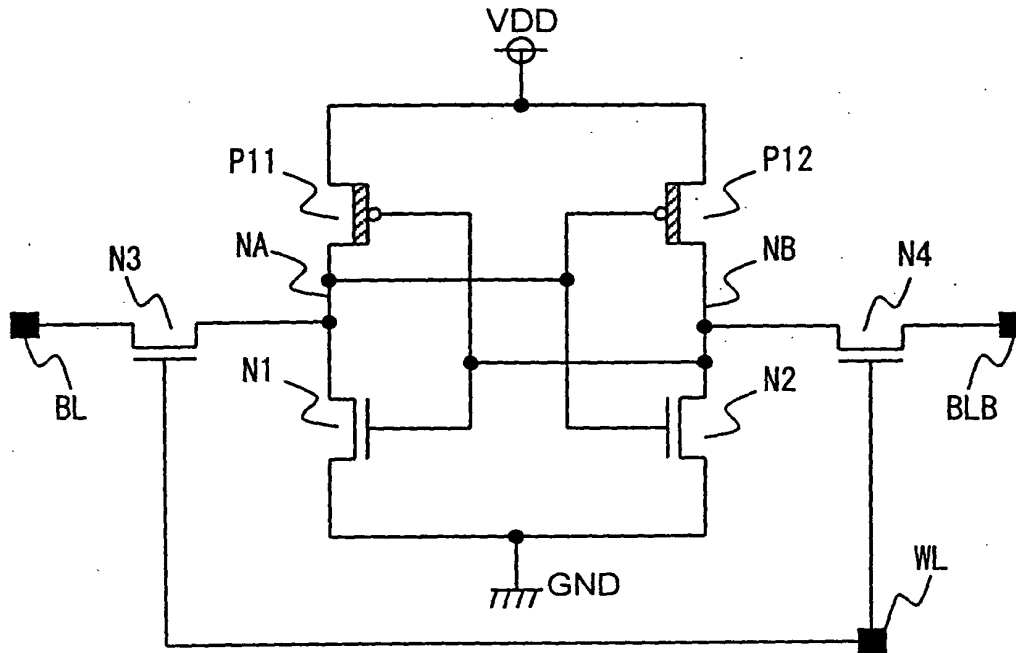


FIG. 2

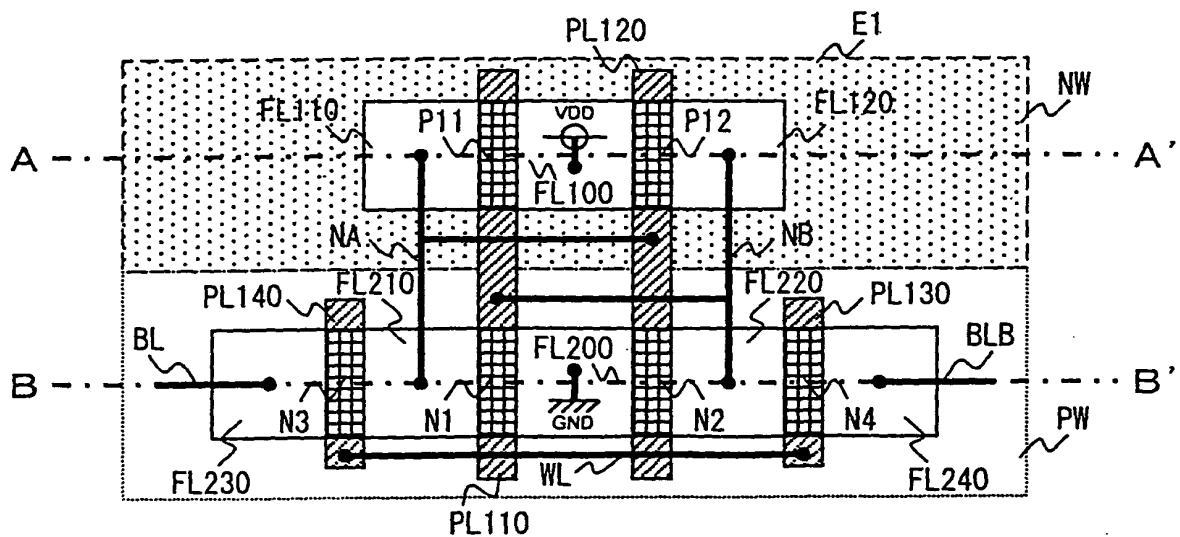


FIG. 3

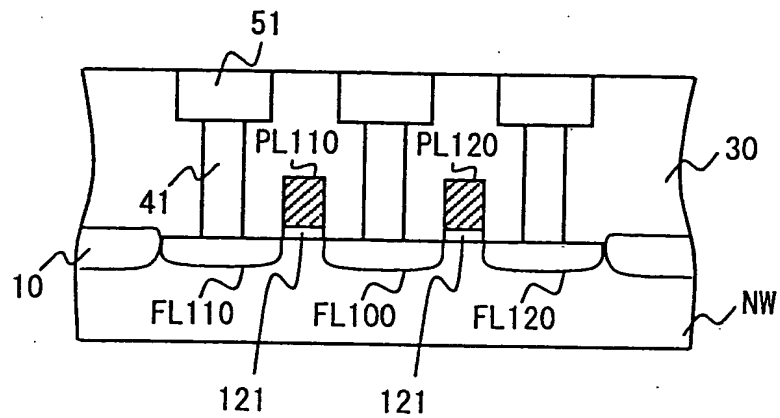


FIG. 4

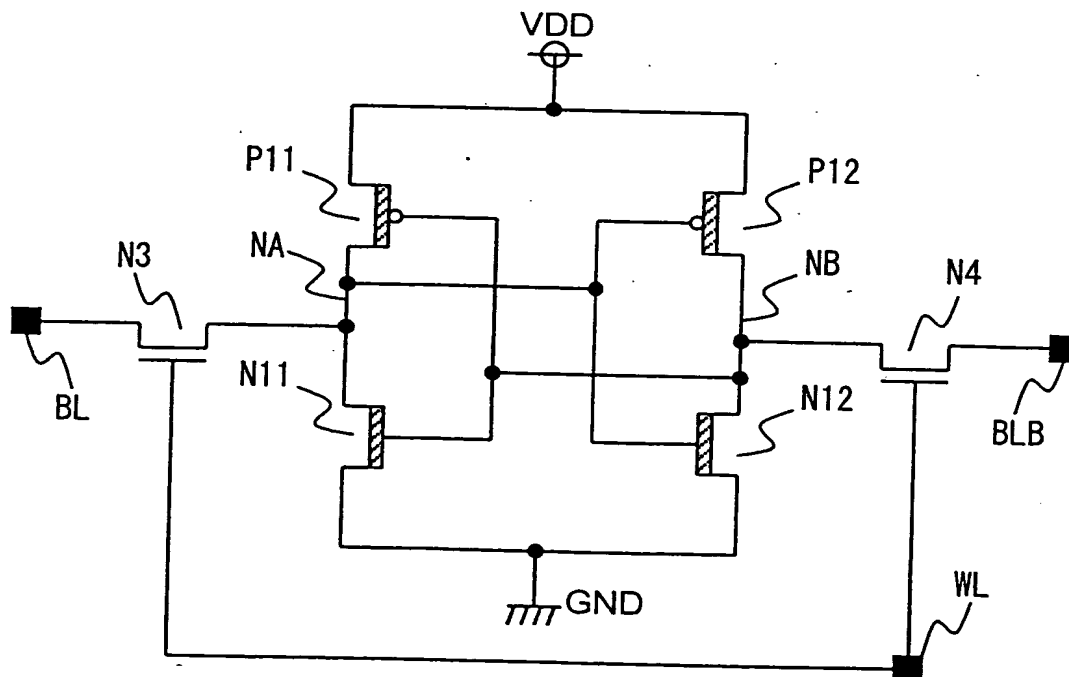


FIG. 5

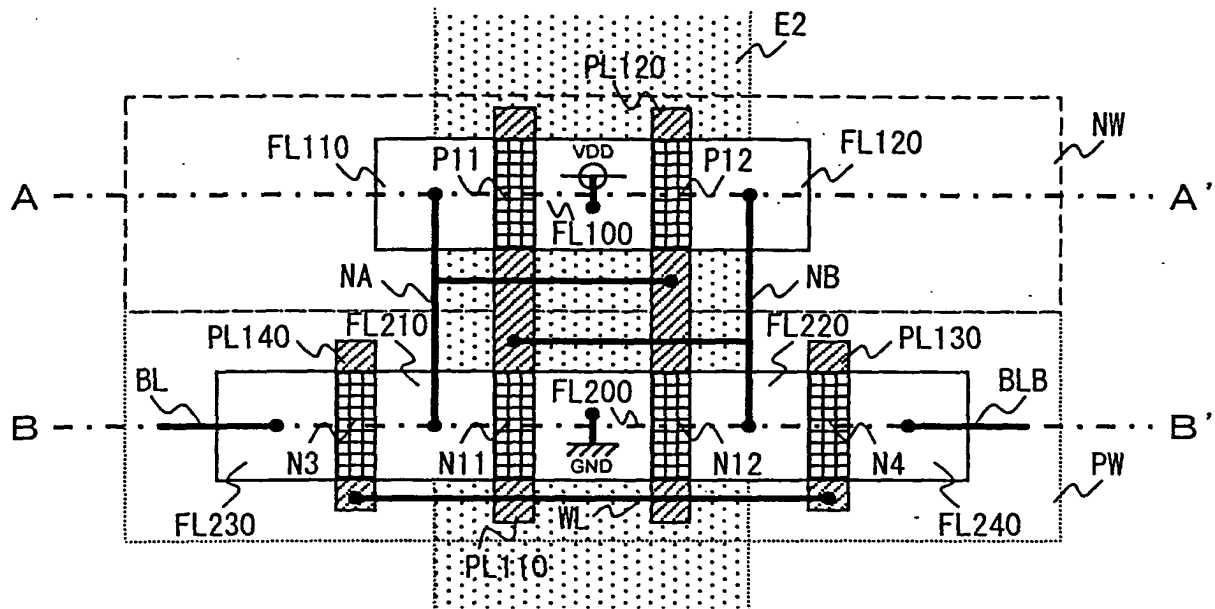


FIG. 6

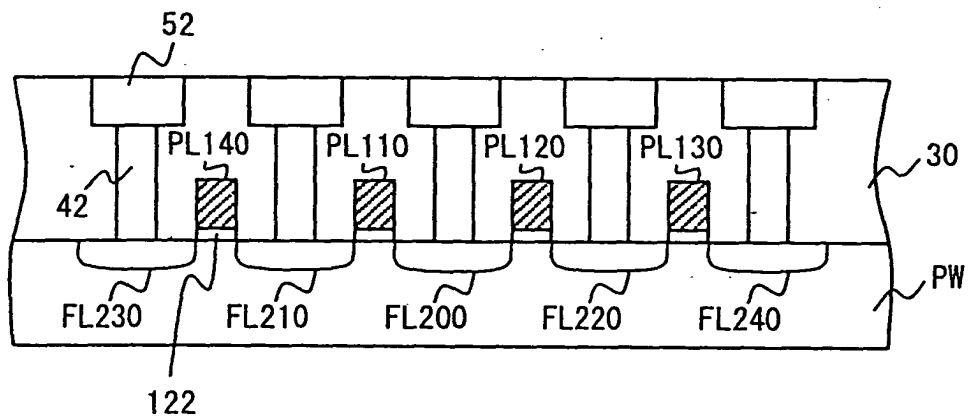


FIG. 7

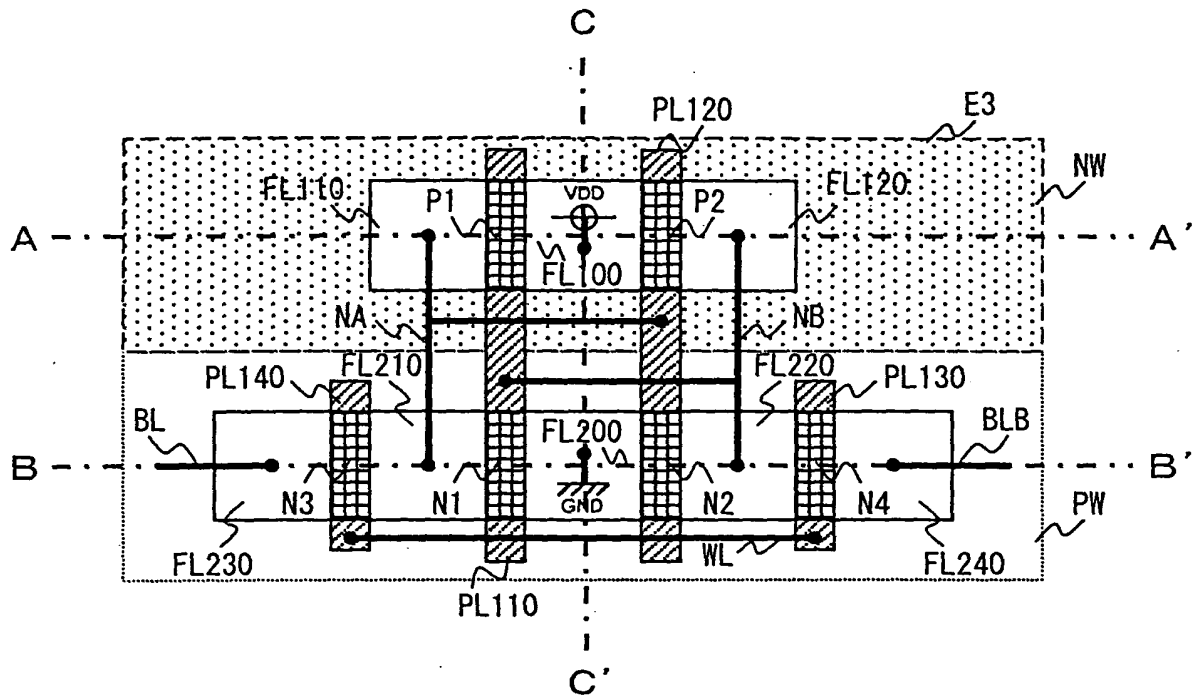
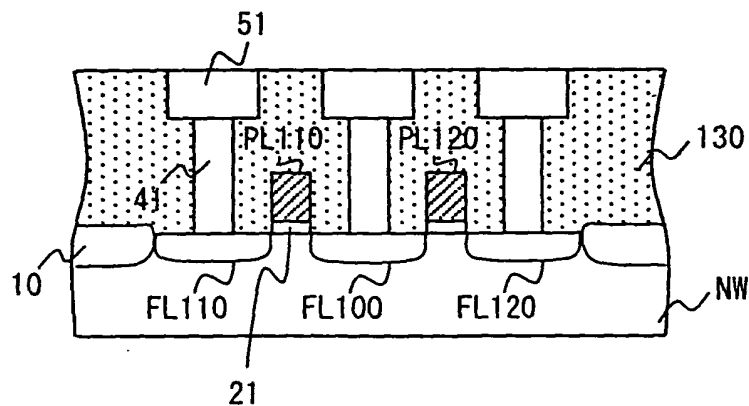


FIG. 8



**TITLE:** A SOFT ERROR RESISTANT SEMICONDUCTOR MEMORY  
DEVICE

**INVENTOR(S): KOJI NII ET AL.**  
**ATTORNEY DOCKET NO: 032404-071**

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FIG. 9

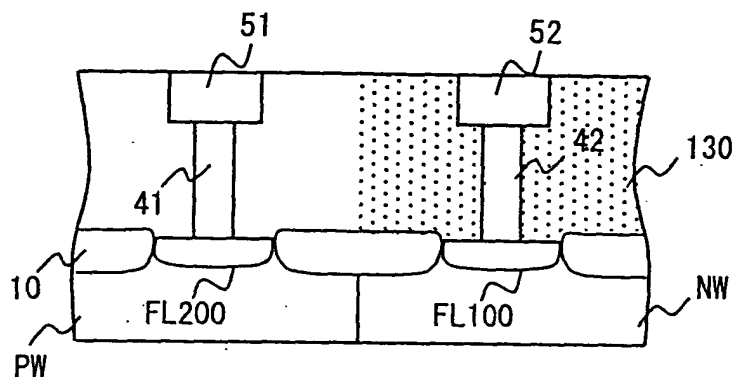


FIG. 10

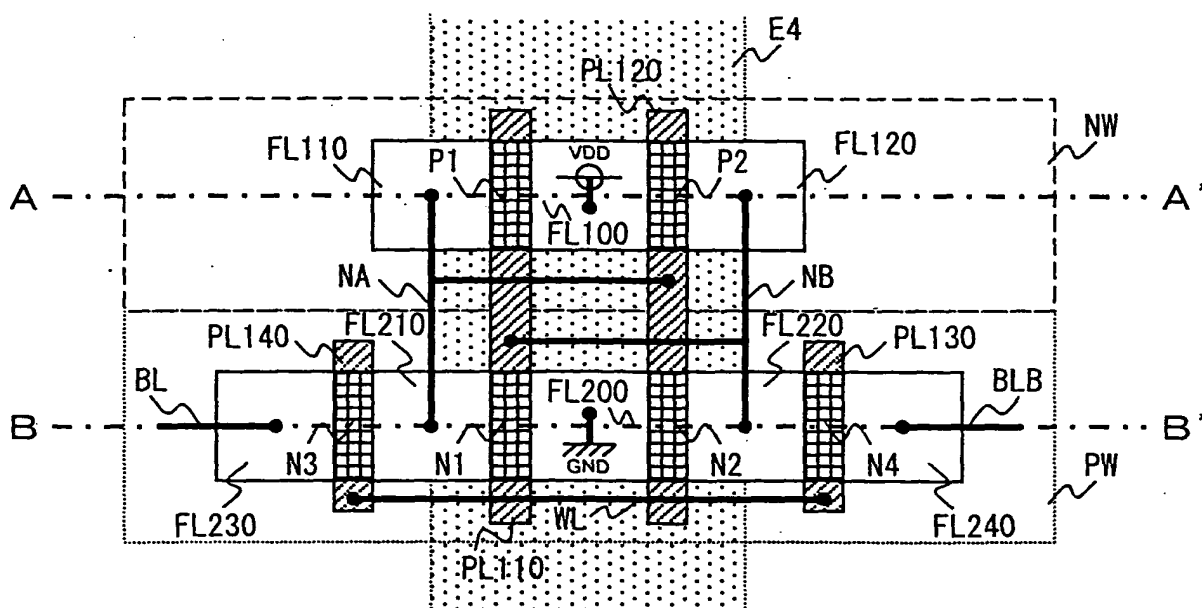


FIG. 11

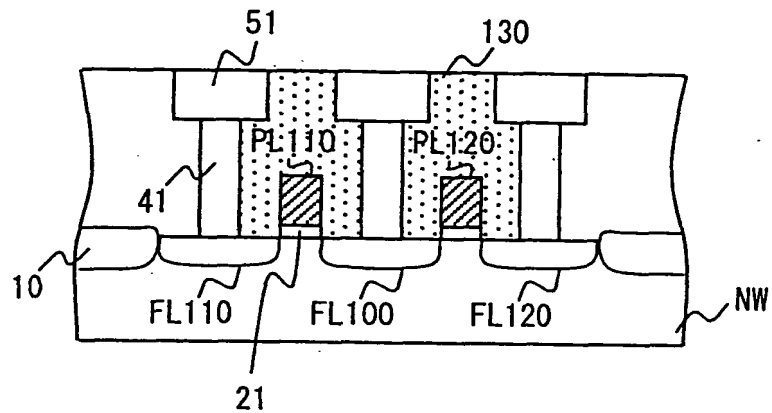


FIG. 12

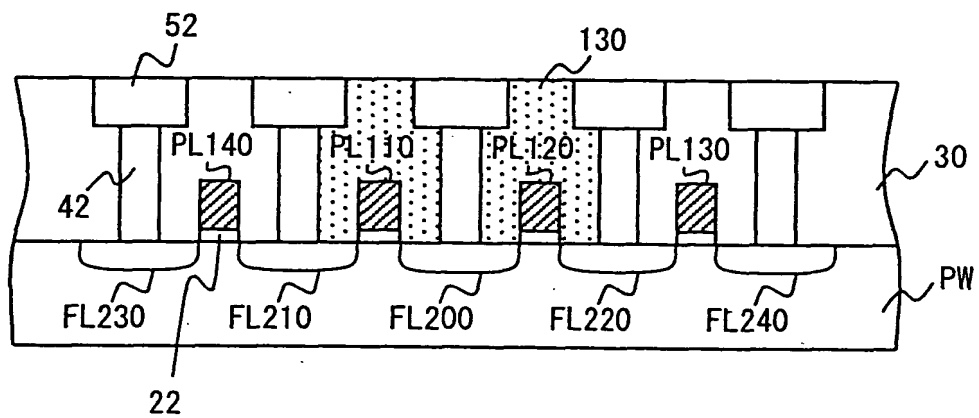


FIG. 13

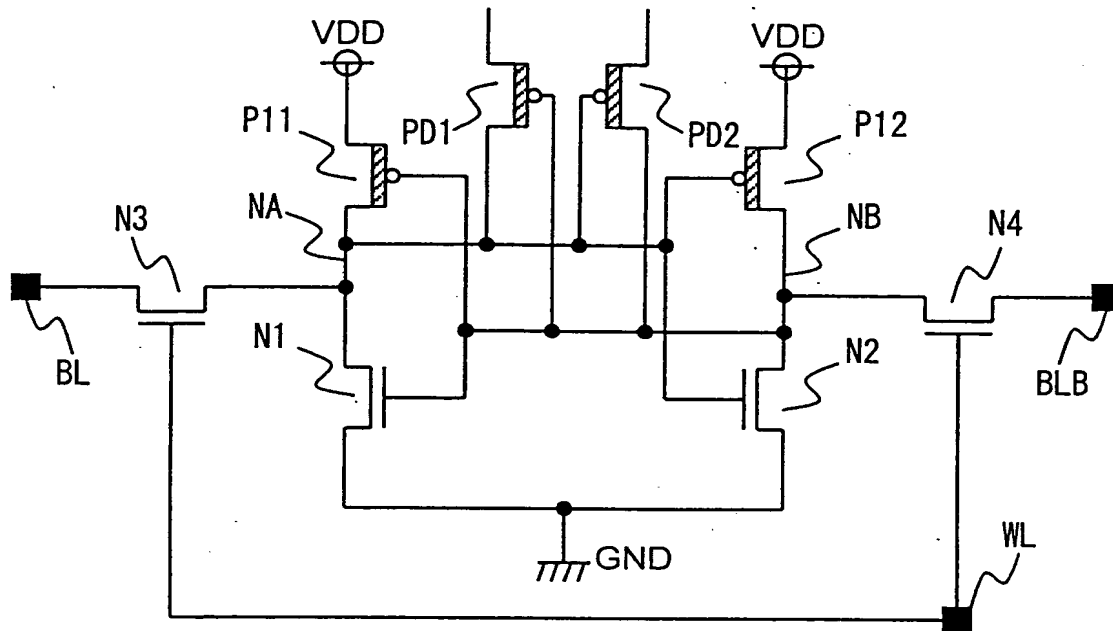


FIG. 14

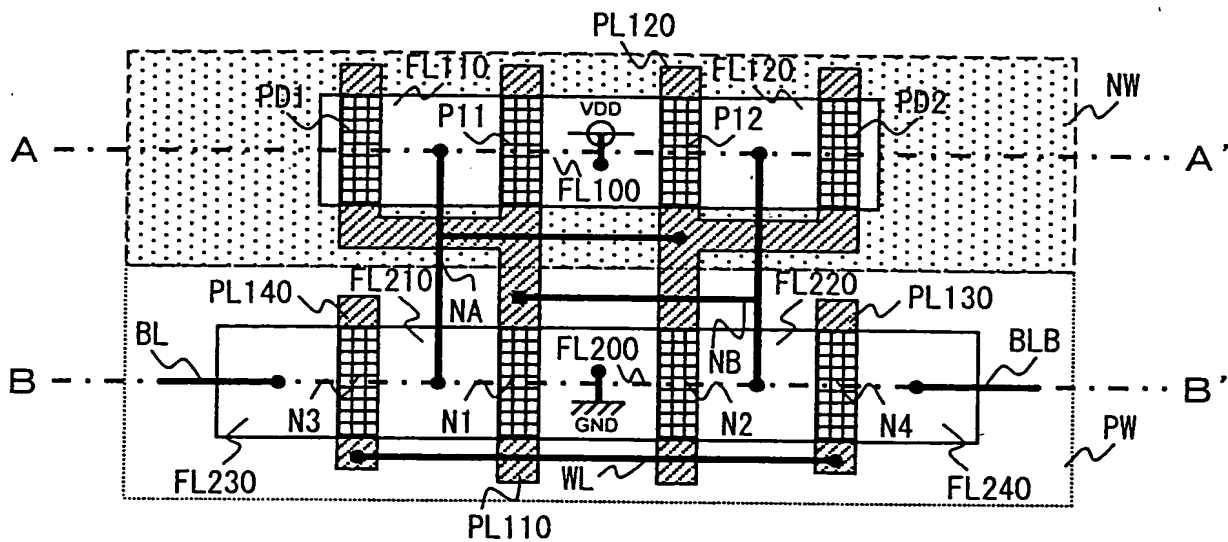
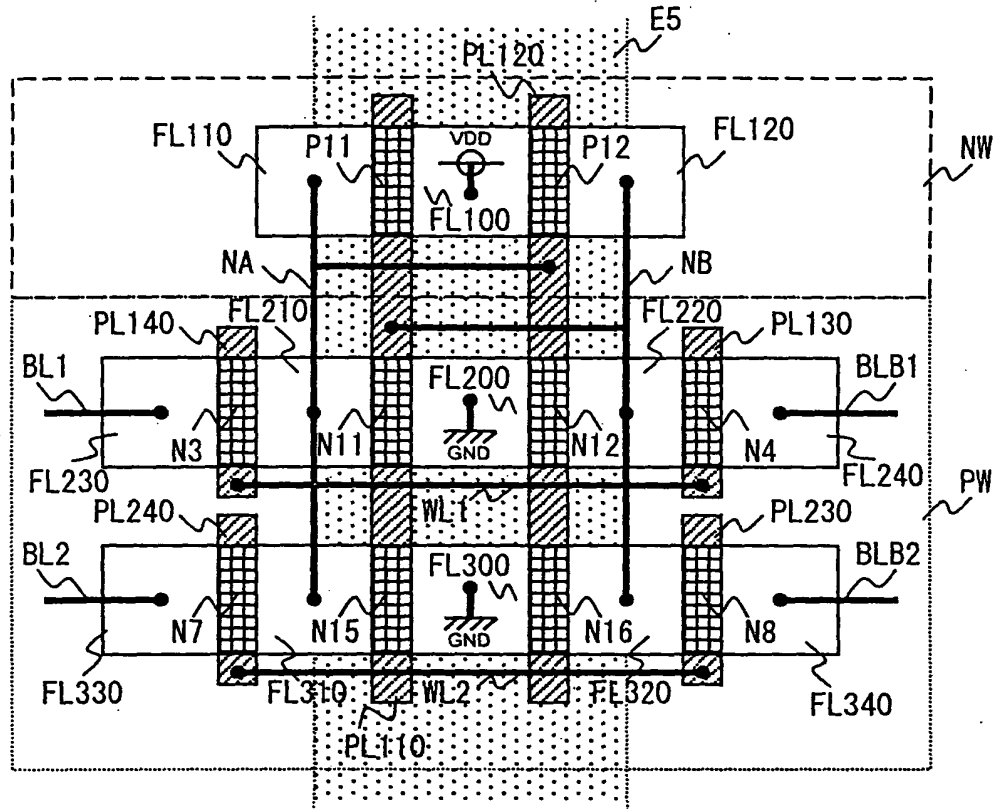


FIG. 15

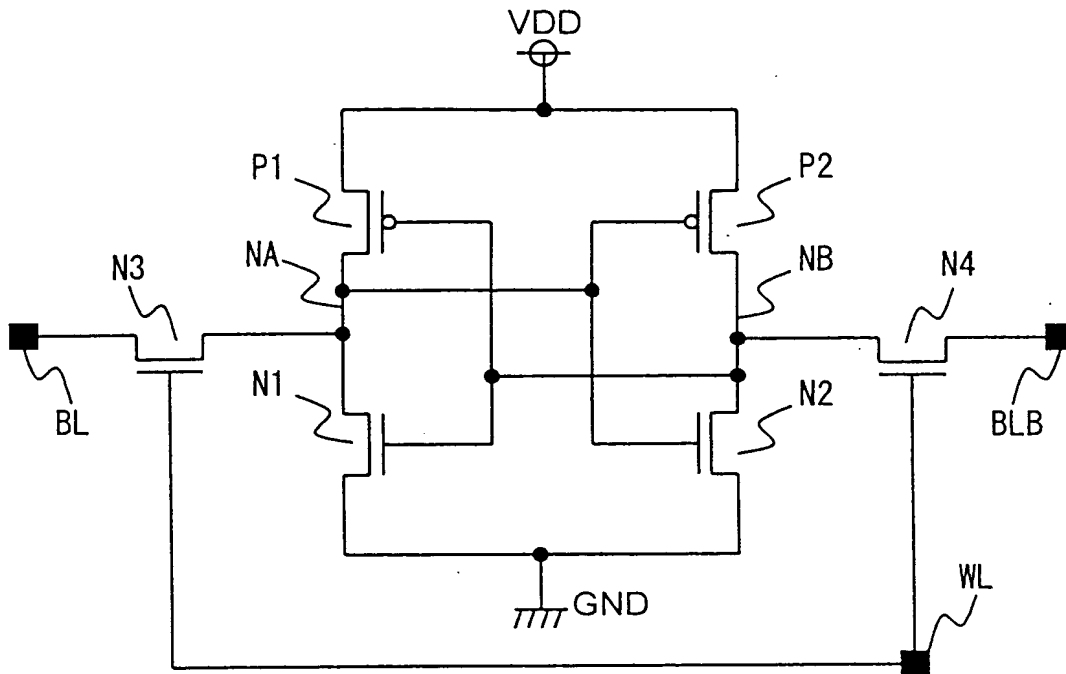
FIG. 16



FIG. 17



**FIG. 18**      **PRIOR ART**



**FIG. 19**      **PRIOR ART**

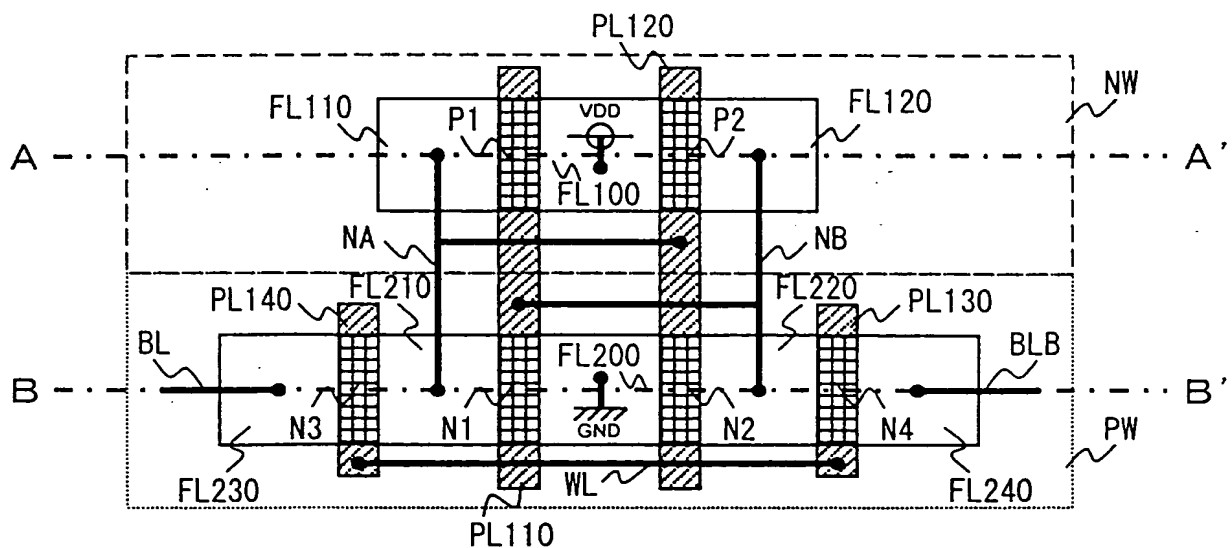


FIG. 20 PRIOR ART

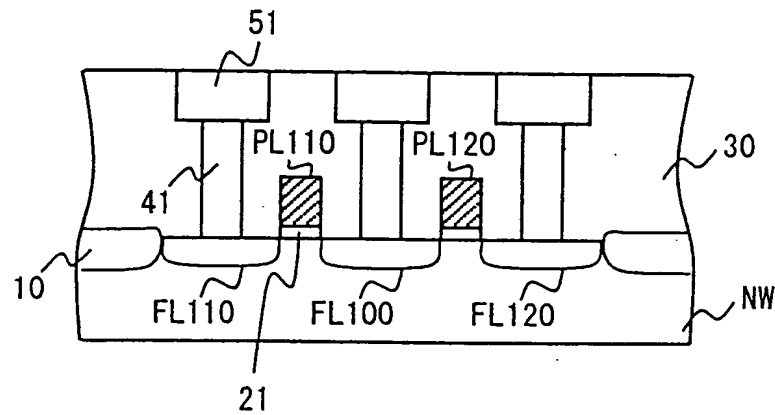


FIG. 21 PRIOR ART

